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Data performance in ATM networks should be measured on packet level instead on cell level, since one or more cell losses within each packet is equivalent to the loss of the entire packet. Two control schemes, packet tail discarding (PTD) and early packet discarding (EPD), were proposed to improve the data performance.

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Performance Analysis in High-Speed Wide Area IP over ATM Networks: Top-to-Bottom End-to-End Monitoring Brian Tierney, William Johnston, Jason Lee, and Gary Hoo Imaging and Distributed Computing Group Ernest Orlando Lawrence Berkeley National Laboratory1 University of California Berkeley, CA, 94720 Abstract

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However, bulk of the workstations remain on Ethernet, generating IP traffic that passes through ATM using special schemes such as PTOp or LANE. In such a network, the performance is affected due to the extra overheads in multiple conversions between cells and packets and managing virtual circuits.

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